

A 2×2 silicon thermo-optic switch based on nanobeam cavities with ultra-small mode volumes

Huanying Zhou, Ciyuan Qiu*, Zhenzhen Xu, Xinhong Jiang, Yuxing Yang, Lei Han, Yong Zhang, and Yikai Su*
State Key Laboratory of Advanced Optical Communication Systems and Networks, Department of Electronic Engineering, Shanghai Jiao Tong University, Shanghai 200240, China *E-mail: qiuciyuan@sjtu.edu.cn; yikaisu@sjtu.edu.cn

Abstract — We propose and experimentally demonstrate a 2×2 thermo-optic (TO) switch implemented by dual photonic crystal nanobeam (PCN) cavities. This structure can achieve low switching power owing to the small mode volumes of the PCN cavities. Extinction ratio of ~15 dB is achieved at through port.

Keywords — integrated optics devices, photonic crystals, optical switching devices.

I. INTRODUCTION

Recent developments in photonic network systems have led to the demand for optical matrix switches with low power consumption and high capacity transmission. A low-power 2×2 optical switch is a critical element for large-scale optical matrix switches [1-2]. Silicon-on-insulator (SOI) offers a very attractive platform to implement optical switches, due to its high index contrast and compatibility with complementary metal oxide semiconductor (CMOS) technology [3]. Thus optical switches based on the relatively large thermo-optic (TO) effect in silicon ($dn/dT = 1.86 \times 10^{-4} \text{ K}^{-1}$) are promising for optical switching systems including a large number of routing channels [4].

To date, several types of silicon based TO switches have been realized by Mach-Zehnder interferometer (MZI), microring resonator (MRR), and so on [5-6]. The main challenges for these TO switches are small device footprint and low switching power, which are affected by the mode volume and the light-matter interaction in the optical device [7]. TO switches based on photonic crystal nanobeam (PCN) cavities could be an effective solution, due to their high quality factor (Q) and small mode volume (V) [8-9].

In this paper, we propose and experimentally demonstrate a novel 2×2 TO switch based on dual silicon PCN cavities. By thermally tuning the refractive index of the silicon, the resonance wavelength of the PCN cavities can be red-shifted, thus the optical switching can be achieved. The PCN cavity has an ultra-small mode volume, leading to only 2- μm -length for thermal tuning in our device. This is 1/60 of that length for a 20- μm -radius microring based TO switch with a switching power of about 20 mW [10]. Since the switching power is proportional to the length for thermal tuning, the switching power of the proposed TO switch is expected to reduce to ~1 mW. An extinction ratio of ~15 dB is achieved at the through port, and the insertion loss of the drop port is only ~0.66 dB in the experiment.

II. OPERATION PRINCIPLE AND DEVICE OPTIMIZATION

Figure. 1(a) shows the basic device schematic structure. The proposed TO switch consists of dual silicon PCN cavities side-coupled to two bus waveguides. For an optimized design of the PCN cavities, two bus waveguides are symmetrically placed

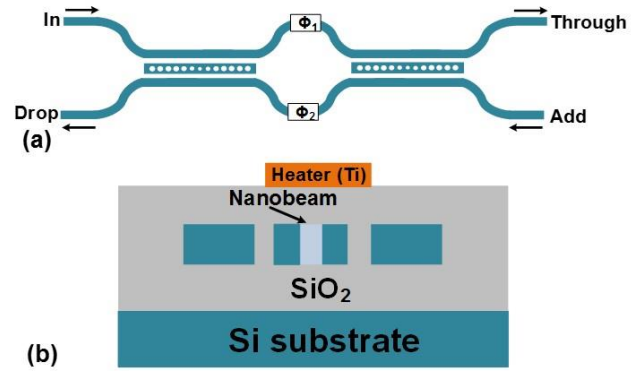


Fig. 1. (a) Schematic diagram of the proposed 2×2 TO switch based on dual PCN cavities. The phase difference between the two arms ($\Phi_1 - \Phi_2$) is equal to π . (b) Cross-section view of the proposed 2×2 TO switch.

at the two sides of a central nanobeam waveguide with equal coupling strengths. The central nanobeam waveguide etched with an array of air-holes forms a Fabry-Perot (F-P) cavity, which consists of a central-taper section and two side-reflector sections [8]. The central-taper section with 13 holes is optimized to reduce the scattering losses and provide high phase matching between the photonic crystal Bloch mode and the waveguide mode. The side-reflector sections are designed as two symmetrical mirrors to reflect light to the central-taper section. To achieve a relatively high Q-factor and a large extinction ratio, the widths of the nanobeam waveguide and the identical bus waveguides are optimized to be 0.565 μm and 0.6 μm , respectively. The gap between the nanobeam waveguide and the bus waveguide is 0.21 μm . The cross-section view of the designed device is shown in Fig. 1(b). A micro-heater is put on the top of the PCN cavity for thermal tuning.

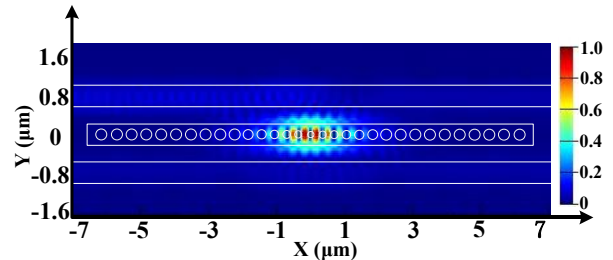


Fig. 2. Calculated electric field distribution of a single PCN cavity.

Based on finite-difference-time-domain (FDTD) method, the mode volume of the single PCN cavity is simulated and shown to approach the fundamental limit of $V = (\lambda/2n)^3$ [9]. The electric field distribution of a single PCN cavity is shown in Fig. 2. It can be seen that the length for thermal tuning is only 2 μm , which is 1/60 of that length for the microring based TO switch with a radius of 20 μm . Thus the switching power of the

proposed TO switch is expected to reduce to ~ 1 mW. And the optical field distribution indicates that the output power transfers equally to each port of the single PCN cavity. In order to realize a high drop transmission at the cross state of the TO switch, the optical switching device is implemented by the cascaded PCN cavities with π phase difference between the two connecting bus waveguides [11].

III. DEVICE FABRICATION AND EXPERIMENT RESULTS

The designed device is fabricated on an SOI wafer with a 220-nm-thick top silicon layer and a 3- μm -thick buried dioxide layer. The micrograph of the fabricated 2×2 TO switch based on

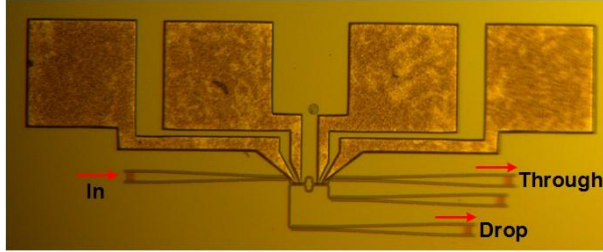


Fig. 3. Micrograph of the fabricated 2×2 TO switch based on dual PCN cavities.

dual PCN cavities is shown in Fig. 3. The device is fabricated by E-beam lithography and reactive ion etching (RIE). A thick layer of 1.5 μm of silicon dioxide is deposited by plasma-enhanced chemical vapor deposition (PECVD). A 100-nm-thick patterned titanium layer is sputtered on the oxide to form the micro-heaters, then a 2- μm -thick aluminum layer is sputtered to form the contact pads.

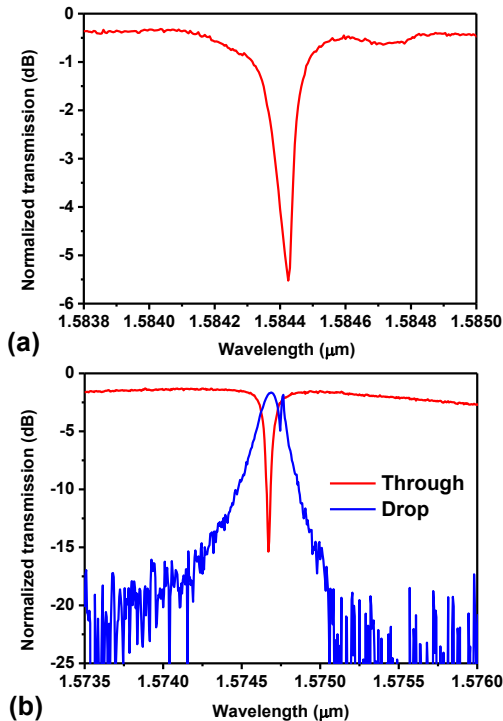


Fig. 4. (a) Normalized transmission spectrum of a fabricated single PCN cavity. (b) Normalized transmission spectra of the fabricated 2×2 nanobeam optical device in through (red line)/drop (blue line) ports.

The optical performance of a single PCN cavity in our fabricated device is firstly evaluated. As shown in Fig. 4(a), the extinction ratio of the transmission spectrum at the through port is ~ 6 dB, which agrees with the simulation results in Fig. 2. Fig. 4(b) shows the normalized transmission spectra of the 2×2 nanobeam optical device. At 1574.54 nm, about 15 dB extinction ratio is obtained at the through port (red line). The insertion loss of the drop port is ~ 0.66 dB at the resonance wavelength (blue line). The full-width-half-maximum (FWHM) of the through port spectrum is ~ 0.08 nm with a quality factor of 20000. The switching power is estimated to be ~ 1 mW. More detailed characterizations and improvement are progressing along the line towards an ultra-compact, low power, and high extinction ratio 2×2 TO switch based on dual PCN cavities.

IV. CONCLUSION

In conclusion, a compact 2×2 TO switch implemented by dual silicon PCN cavities has been experimentally demonstrated. About 15-dB extinction ratio at the through port and 0.66-dB insertion loss of the drop port are achieved in the experiment. Feasibility study indicates that the switching power of the proposed TO switch can be reduced to ~ 1 mW.

ACKNOWLEDGEMENT

This work was supported in part by the National Natural Science Foundation of China under Grant 61235007, in part by the 863 High-Tech Program under Grant 2015AA015503/2015AA017001, and in part by the Natural Science Foundation of Shanghai under Grant 15ZR1422800. We acknowledge the helpful discussions with Richard Soref, and the support of device fabrication by the Center for Advanced Electronic Materials and Devices of Shanghai Jiao Tong University.

REFERENCES

- [1] S. J. Ben Yoo, "Optical packet and burst switching technologies for the future photonic internet," *J. Lightwave Technol.*, vol. 24, no. 12, 4468-4492 2006.
- [2] D. Van Thourhout, T. Spuesens, S. K. Selvaraja, L. Liu, and L. Grenouillet, "Nanophotonic Devices for Optical Interconnect," *IEEE Journal of Selected Topics in Quantum Electronics*, vol. 16, no. 5, 1363-1375, 2010.
- [3] L. Chen and Y. K. Chen, "Compact, low-loss and low-power 8×8 broadband silicon optical switch," *Opt. Express*, vol. 20, no. 17, 18977-18985, 2012.
- [4] R. L. Espinola, M.-C. Tsai, J. T. Yardley, and R. M. Osgood, Jr., "Fast and low-power thermo-optic switch on thin silicon-on-insulator," *IEEE Photon. Technol. Lett.* vol. 15, no. 10, 1366-1368, 2003.
- [5] M. Harjanne, M. Kapulainen, T. Aalto, and P. Heimala, "Sub- μs switching time in silicon-on-insulator Mach-Zehnder thermo-optic switch," *IEEE Photon. Technol. Lett.* vol. 16, 2039-2041, 2004.
- [6] I. Kiyat, A. Aydinli, and N. Dagli, "Low-power thermo-optical tuning of SOI resonator switch," *IEEE Photon. Technol.* vol. 18, no. 2, 364-366, 2006.
- [7] X. Wang, J. A. Martinez, M. S. Nawrocka, and R. R. Panepucci, "Compact thermally tunable silicon wavelength switch: modeling and characterization," *IEEE Photon. Technol. Lett.* vol. 20, no. 11, 936-938, 2008.
- [8] X. Ge, Y. Shi, and S. He, "Ultra-compact channel drop filter based on photonic crystal nanobeam cavities utilizing a resonant tunneling effect," *Opt. Lett.* vol. 39, no. 24, 6973-6976, 2014.
- [9] Q. Quan, D. Floyd, I. Burgess, P. Deotare, I. Frank, S. Tang, R. Ilic, and M. Loncar, "Single particle detection in CMOS compatible photonic crystal nanobeam cavities," *Opt. Express*, vol. 23, no. 18, 32225-32233, 2013.
- [10] X. Li, H. Xu, X. Xiao, Z. Li, Y. Yu, and J. Yu, "Fast and efficient silicon thermo-optic switching based on reverse breakdown of pn junction," *Opt. Lett.* vol. 39, no. 4, 751-753, 2014.
- [11] H. Zhou, C. Qiu, J. Wu, B. Liu, X. Jiang, J. Peng, Z. Xu, R. Liu, Y. Zhang, Y. Su, and R. Soref, " 2×2 Electro-optic Switch with fJ/bit Switching Power Based on Dual Photonic Crystal Nanobeam Cavities," in *Proc. CLEO*, 2016, paper JTh2A.24.